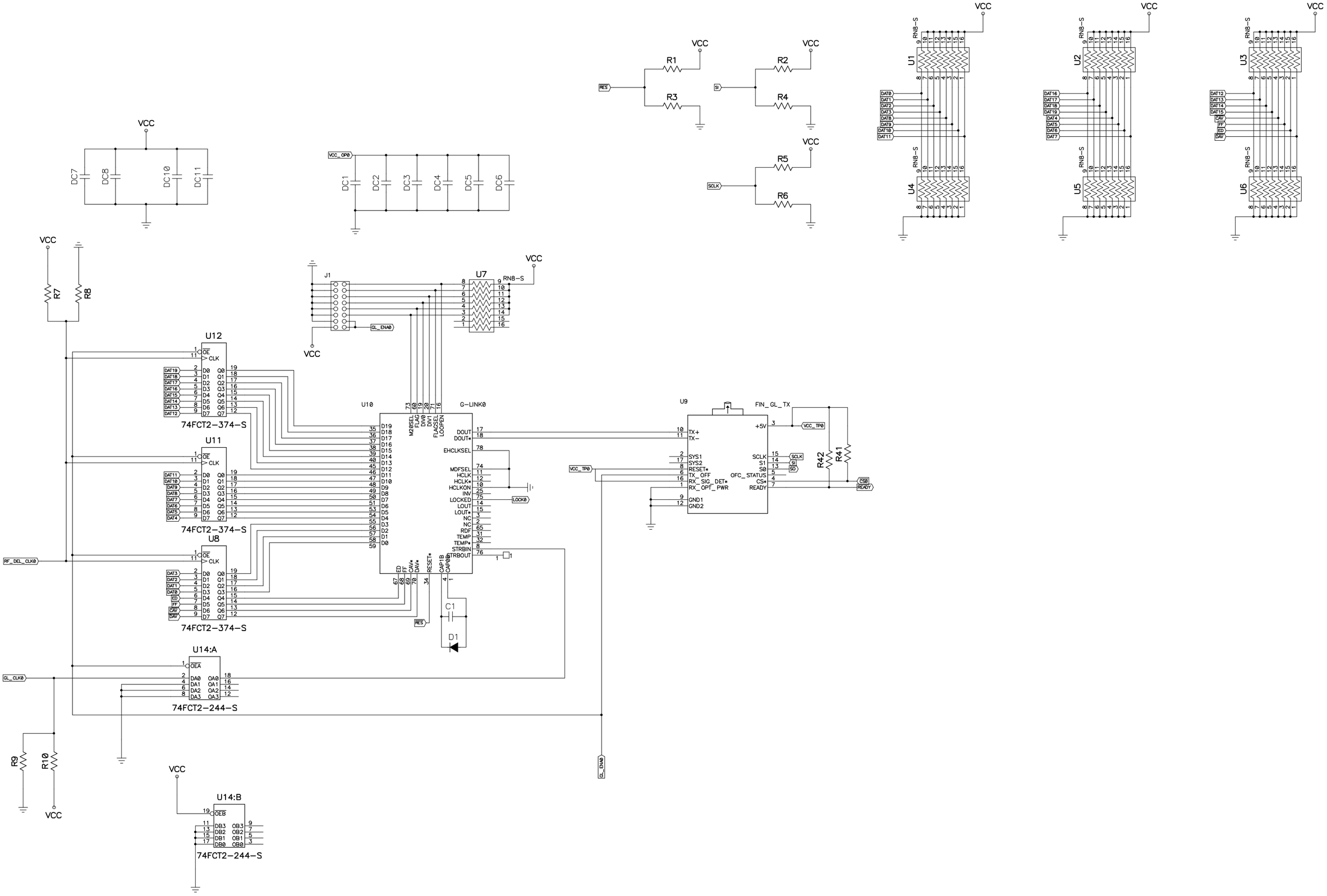
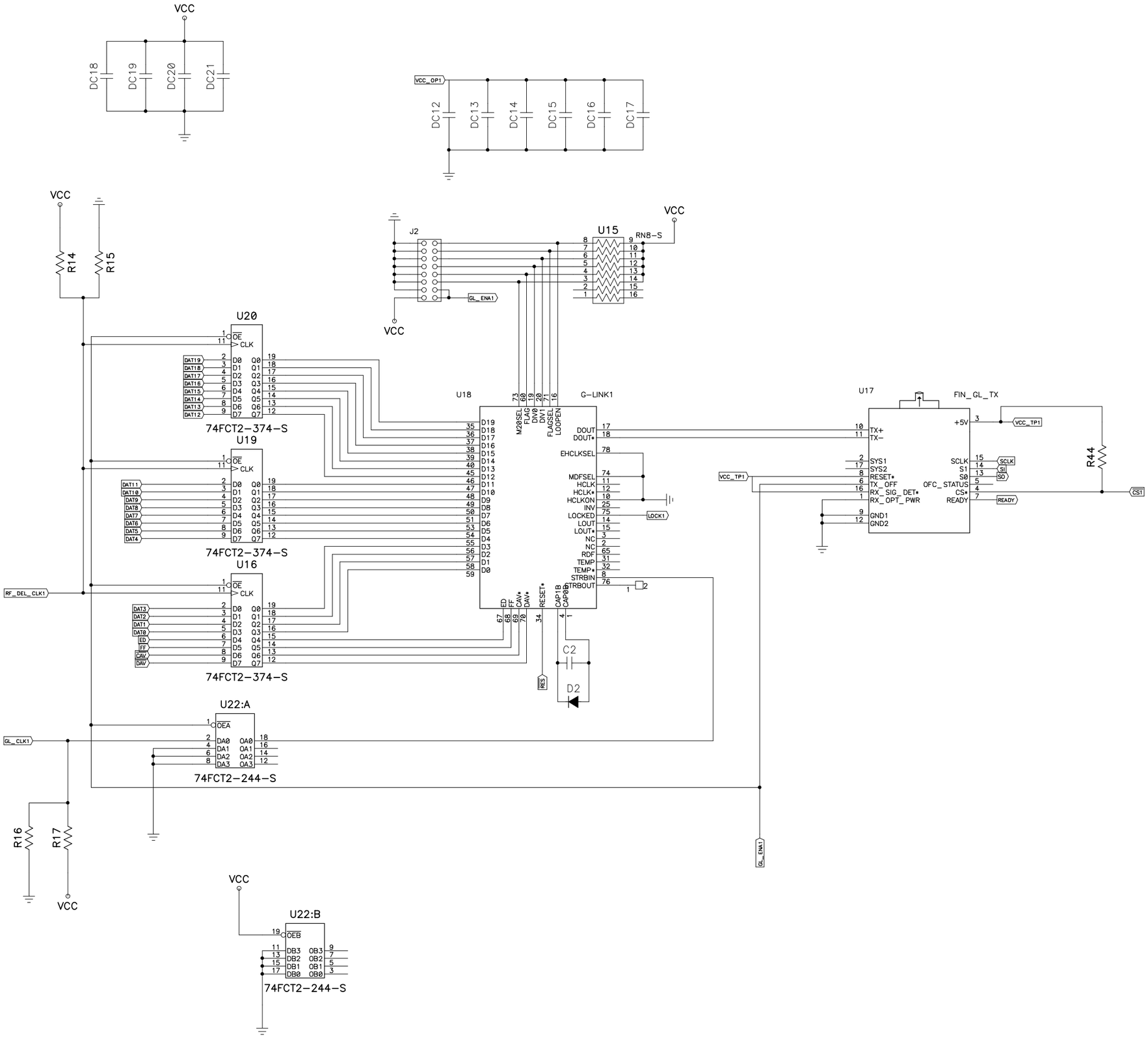
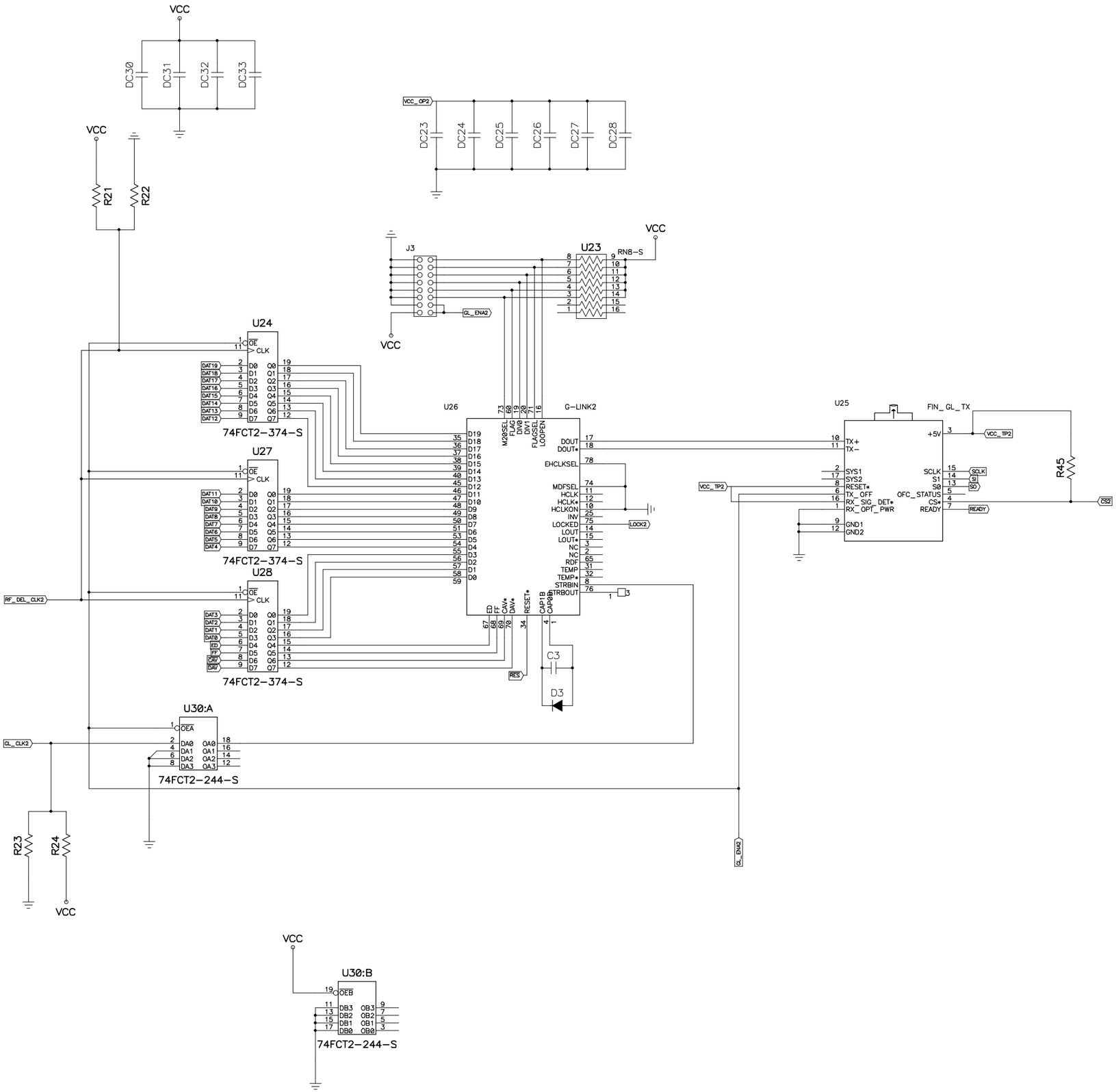
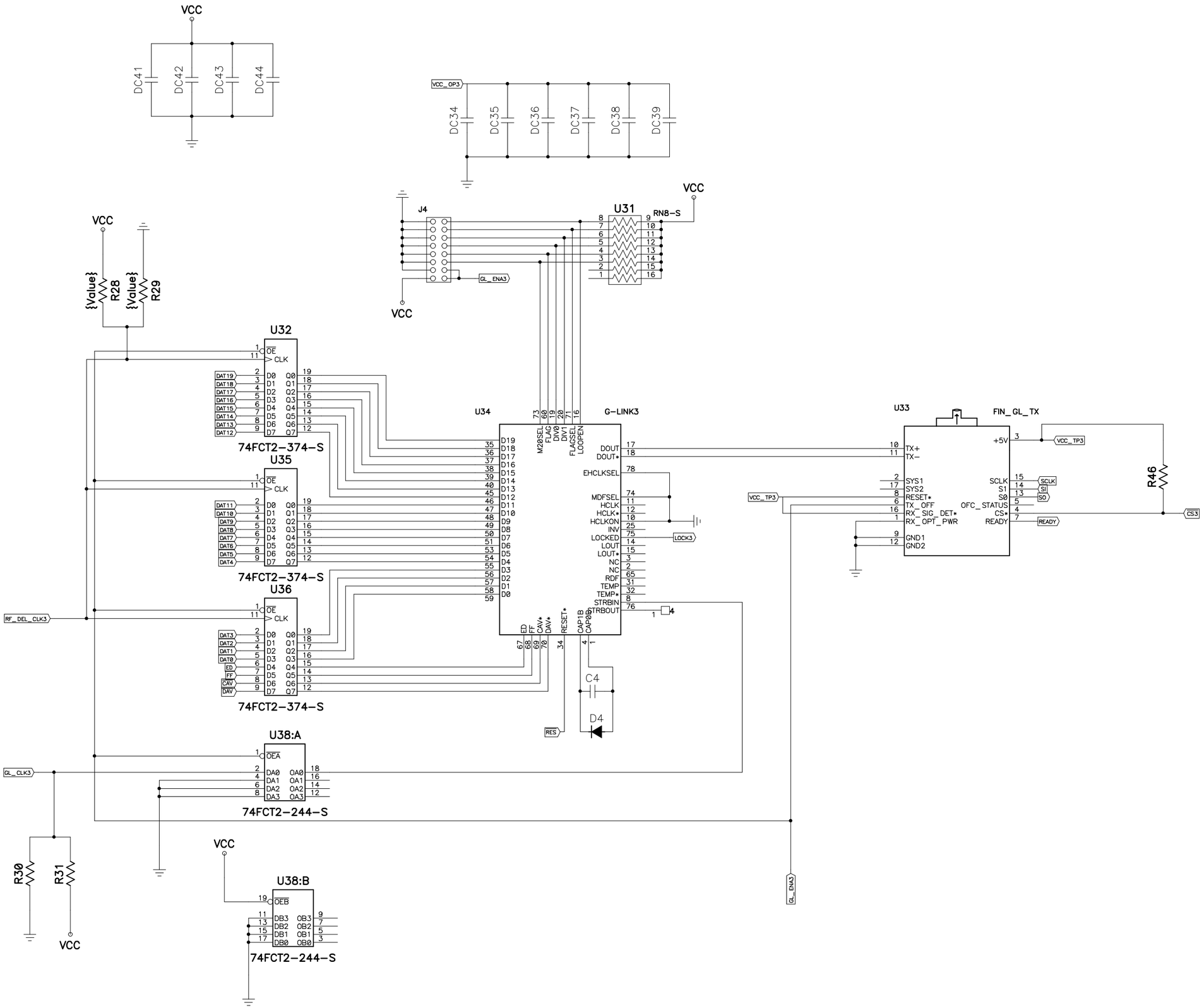
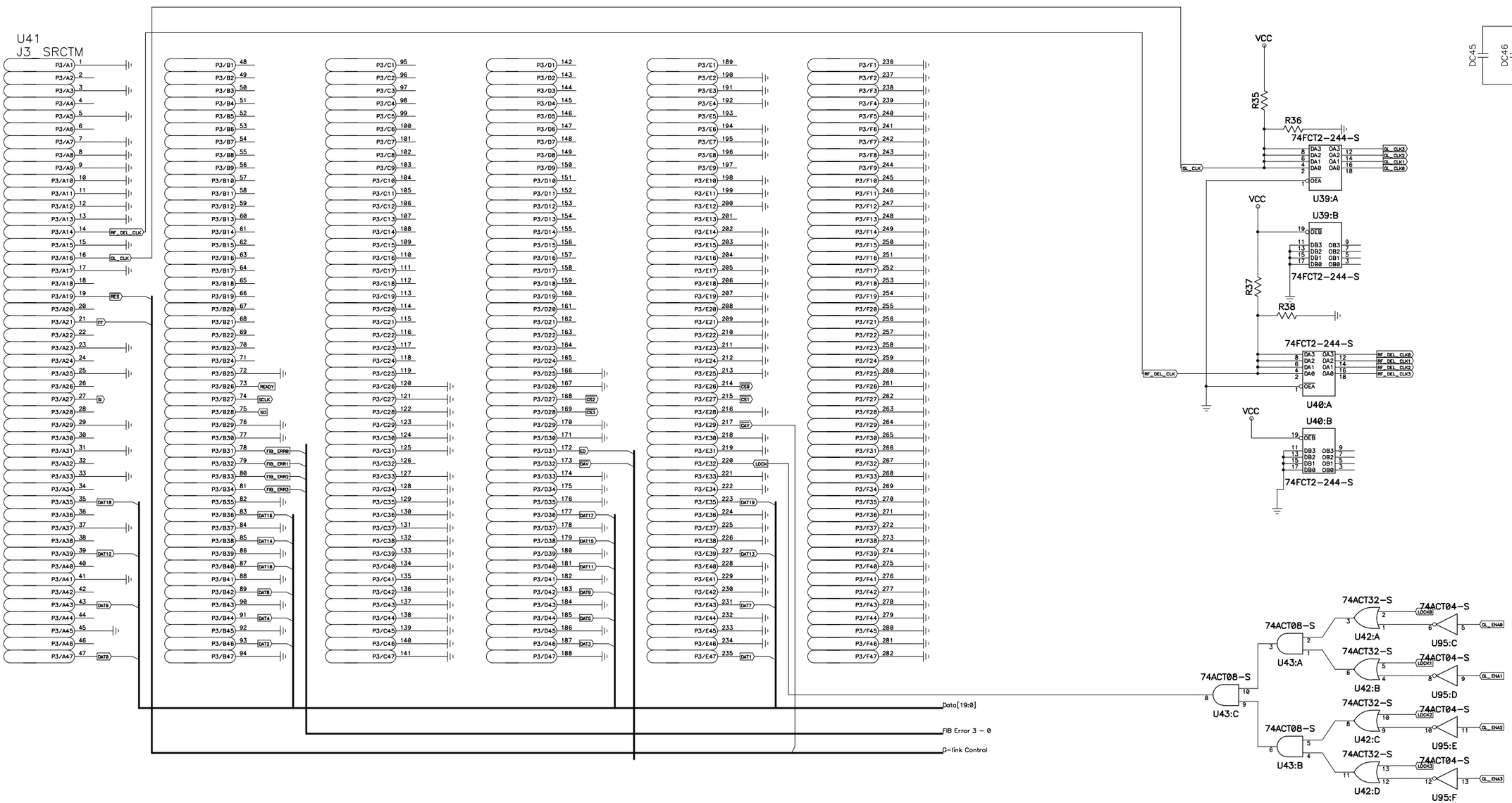


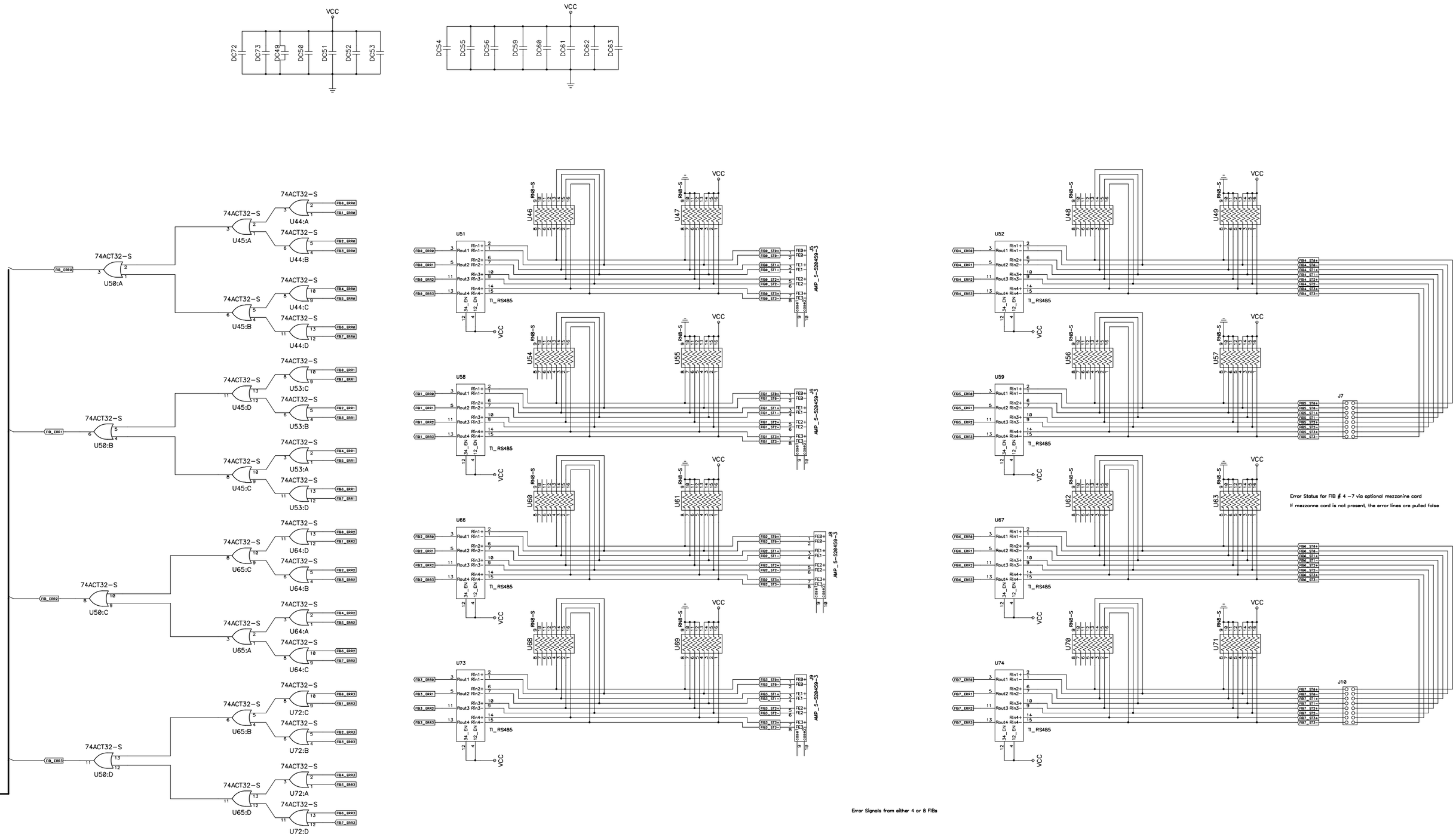
VCC
R1
R2
R3
R4
R5
R6
RES
SI
SCLK
U1
U2
U3
U4
U5
U6
RN8-S
DC1
DC2
DC3
DC4
DC5
DC6
DC7
DC8
DC10
DC11
VCC_OP0
R7
R8
J1
U7
GL_ENA0
U12
U11
U8
U10
G-LINK0
74FCT2-374-S
U9
FIN_GL_TX
VCC_TP0
R42
R41
CS0
READY
LOCK0
RF_DEL_CLK0
C1
D1
U14:A
74FCT2-244-S
GL_CLK0
R9
R10
U14:B

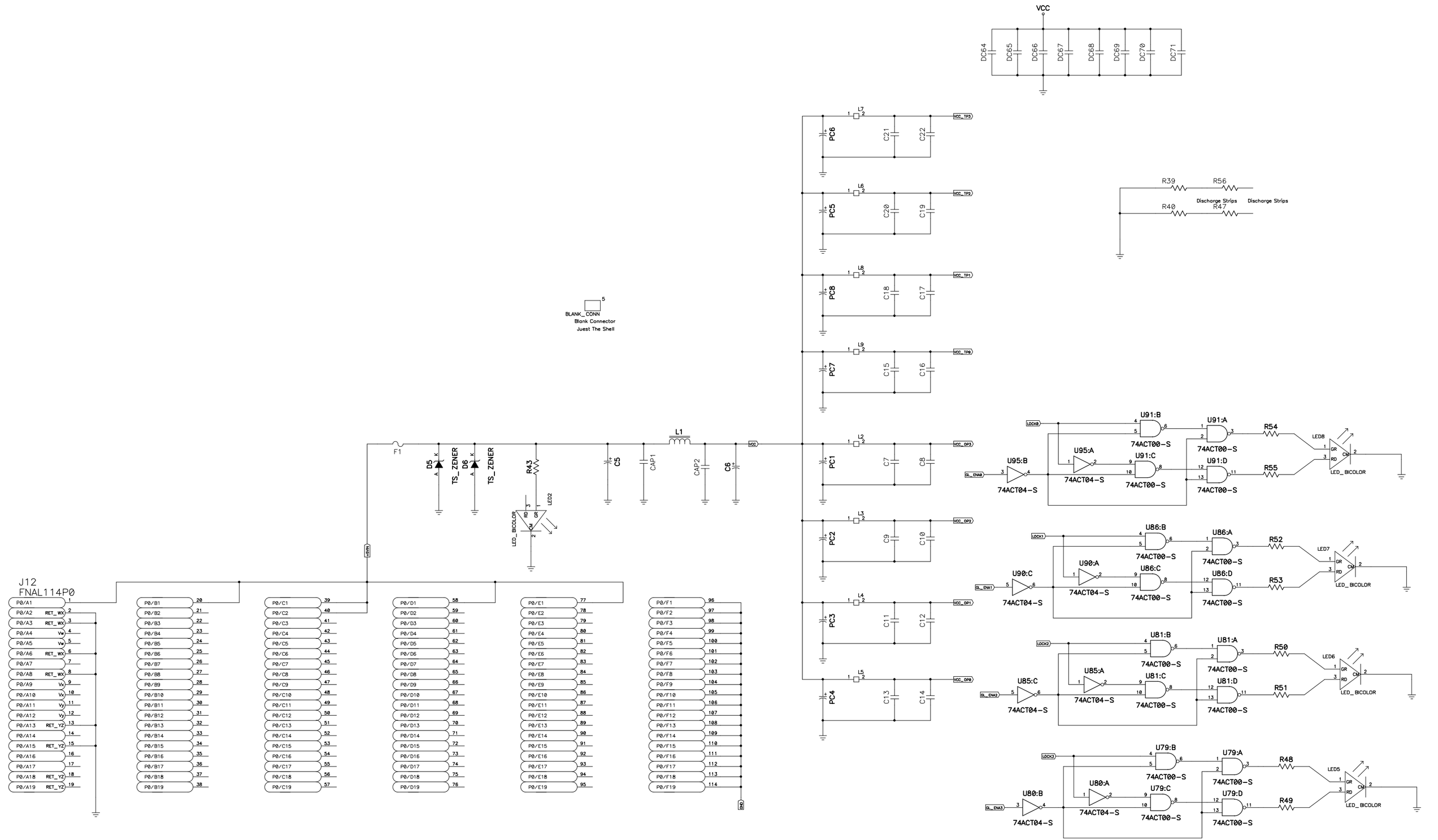

VCC
DC18
DC19
DC20
DC21
VCC_OP1
DC12
DC13
DC14
DC15
DC16
DC17
R14
R15
J2
GL_ENA1
U15
RN8-S
U20
74FCT2-374-S
U19
74FCT2-374-S
U16
74FCT2-374-S
RF_DEL_CLK1
U18
G-LINK1
M20SEL
FLAG
DIV0
DIV1
FLAGSEL
LOOPEN
DOUT
DOUT*
EHCLKSEL
MDFSEL
HCLK
HCLK*
HCLKON
INV
LOCKED
LOUT
LOUT*
NC
RDF
TEMP
TEMP*
STRBIN
STRBOUT
LOCK1
RESET*
CAP1B
CAP0B
C2
D2
RES
U22:A
74FCT2-244-S
GL_CLK1
R16
R17
U17
FIN_GL_TX
VCC_TP1
TX+
TX-
SYS1
SYS2
RESET*
TX_OFF
RX_SIG_DET*
RX_OPT_PWR
GND1
GND2
+5V
SCLK
S1
S0
OFC_STATUS
CS*
READY
R44
CS1
U22:B
74FCT2-244-S


VCC
DC30
DC31
DC32
DC33
VCC_OP2
DC23
DC24
DC25
DC26
DC27
DC28
R21
R22
J3
U23
RN8-S
GL_ENA2
U24
74FCT2-374-S
U27
U28
U26
G-LINK2
U25
FIN_GL_TX
VCC_TP2
R45
CS2
SCLK
SI
SO
READY
LOCK2
RF_DEL_CLK2
RES
C3
D3
U30:A
74FCT2-244-S
GL_CLK2
R23
R24
U30:B


VCC
DC41
DC42
DC43
DC44
VCC_OP3
DC34
DC35
DC36
DC37
DC38
DC39
J4
U31
RN8-S
GL_ENA3
VCC
{Value}
R28
R29
U32
74FCT2-374-S
U35
U36
RF_DEL_CLK3
U34
G-LINK3
M20SEL
FLAG
DIV0
DIV1
FLAGSEL
LOOPEN
DOUT
DOUT*
EHCLKSEL
MDFSEL
HCLK
HCLK*
HCLKON
INV
LOCKED
LOUT
LOUT*
NC
RDF
TEMP
TEMP*
STRBIN
STRBOUT
CAP1B
CAP0B
RESET*
ED
FF
CAV*
DAV*
LOCK3
RES
C4
D4
U33
FIN_GL_TX
TX+
TX-
SYS1
SYS2
RESET*
TX_OFF
RX_SIG_DET*
RX_OPT_PWR
GND1
GND2
+5V
VCC_TP3
SCLK
S1
S0
OFC_STATUS
CS*
READY
SI
SO
R46
CS3
GL_CLK3
U38:A
74FCT2-244-S
R30
R31
U38:B
GL_ENA3


U41
J3_SRCTM
RF_DEL_CLK
GL_CLK
RES
FF
SI
READY
SCLK
SO
FIB_ERR0
FIB_ERR1
FIB_ERR2
FIB_ERR3
DAT18
DAT16
DAT14
DAT12
DAT10
DAT8
DAT6
DAT4
DAT2
DAT0
DAT19
DAT17
DAT15
DAT13
DAT11
DAT9
DAT7
DAT5
DAT3
DAT1
CS0
CS1
CS2
CS3
ED
DAV
LOCK
Data[19:0]
FIB Error 3 – 0
G-link Control
VCC
R35
R36
74FCT2-244-S
U39:A
U39:B
R37
R38
U40:A
U40:B
DC45
DC46
DC47
DC48
DC75
DC74
DC58
DC57
DC77
DC76
74ACT08-S
U43:A
U43:B
U43:C
74ACT32-S
U42:A
U42:B
U42:C
U42:D
74ACT04-S
U95:C
U95:D
U95:E
U95:F
GL_ENA0
GL_ENA1
GL_ENA2
GL_ENA3


VCC
DC72
DC73
DC49
DC50
DC51
DC52
DC53
DC54
DC55
DC56
DC59
DC60
DC61
DC62
DC63
74ACT32-S
U50:A
U50:B
U50:C
U50:D
U44:A
U44:B
U44:C
U44:D
U45:A
U45:B
U45:C
U45:D
U53:A
U53:B
U53:C
U53:D
U64:A
U64:B
U64:C
U64:D
U65:A
U65:B
U65:C
U65:D
U72:A
U72:B
U72:C
U72:D
FIB_ERR0
FIB_ERR1
FIB_ERR2
FIB_ERR3
FIB Error 3 – 0
U51
U52
U58
U59
U66
U67
U73
U74
U46
U47
U48
U49
U54
U55
U56
U57
U60
U61
U62
U63
U68
U69
U70
U71
RN8-S
TI_RS485
J5
J6
J7
J8
J9
J10
AMP_5-520459-3
Error Status for FIB # 4 – 7 via optional mezzanine card
If mezzanine card is not present, the error lines are pulled false
Error Signals from either 4 or 8 FIBs



VCC
DC64
DC65
DC66
DC67
DC68
DC69
DC70
DC71
L7
PC6
C21
C22
VCC_TP3
L6
PC5
C20
C19
VCC_TP2
L8
PC8
C18
C17
VCC_TP1
L9
PC7
C15
C16
VCC_TP0
R39
R56
Discharge Strips
Discharge Strips
R40
R47
BLANK_CONN
Blank Connector
Juest The Shell
F1
D5
TS_ZENER
D6
TS_ZENER
R43
LED2
LED_BICOLOR
C5
CAP1
L1
CAP2
C6
VCC
L2
PC1
C7
C8
VCC_OP3
L3
PC2
C9
C10
VCC_OP2
L4
PC3
C11
C12
VCC_OP1
L5
PC4
C13
C14
VCC_OP0
J12
FNAL114P0
U91:B
U91:A
U95:B
U95:A
U91:C
U91:D
74ACT00-S
74ACT04-S
R54
R55
LED8
LED_BICOLOR
U86:B
U86:A
U90:C
U90:A
U86:C
U86:D
R52
R53
LED7
U81:B
U81:A
U85:C
U85:A
U81:C
U81:D
R50
R51
LED6
U79:B
U79:A
U80:B
U80:A
U79:C
U79:D
R48
R49
LED5